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pin pull-up:orcad capture enables the user to designate certain pins on the schematic to be internally or externally pulled up. this is useful when the schematic has a large number of net names or if the user wants to have a particular pin that is not connected to anything to be pulled up to a specified voltage level. ip formats:orcad pspice now supports three major ip formats: hdl, verilog, and vhdl. this is an enhancement over the previous release where only hdl and verilog were supported. the new vhdl format helps to generate ip that can be used in any cad system. the orcad pspice 16.6 qir 3 is faster, allowing you to open more projects simultaneously. in addition, the pspice 16.6 qir 3 is now available as a web app, so you can use it from any browser, on any platform. this enables you to use the interactive features, such as the fex editor and output windows, which are not available when using the standalone application. if you have orcad capture web browser running, the browser remembers the last visited page. when you navigate to a new page, the previously visited page is displayed. note, all browser tabs remain open throughout the upgrade process. if you are upgrading from a previous version of orcad capture, you may need to close any open browser tabs before the upgrade can be completed successfully. if you are upgrading from orcad capture 16.5, there is no need to close any open browser tabs. pspice version 14.6 is the minimum requirement for the windows platform. if you are running orcad on a mac, version 10.9 is required. version 10.10 is required if you are running orcad pspice simulator on mac os.

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orcad open source: orcad is an open source product available under the gnu general public license version 2 or later. orcad is available at . you can download the latest source code and development tools for orcad at . digital compound function:the digital compound function in orcad pspice provides a direct, fast, and accurate means to create digital circuits composed of digital components. digital compound handles high frequencies in a similar manner to analog compound, with the added benefit of having unlimited resolution. new sequence function:orcad capture has provided the user with the ability to create sequences of arbitrary length. this is a new feature as compared to previous versions of orcad capture, which only allowed sequential blocks of 1, 2, 3, and 4 gates. the new sequence function allows the user to create any arbitrary length of sequential blocks. it has been made extremely easy to use and the new analysis interface simplifies the creation and analysis of such sequences. design frameworkthe orcad design framework 17.4-2019 provides the user with a consistent user experience that is similar to that of the circuit designer. the design framework helps the user to create and design a schematic, layout, pcb, ip, and 3d design in a unified way. the design framework provides the user with a number of features including automatic renaming of pins and blocks, auto-detection of block and pin group names, automatic naming of net groups, auto-detection of net groups and the ability to add external components. all these features can be configured to suit a particular user's preferences. 5ec8ef588b

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